

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Wing K Luk, et al.

Examiner: Bernstein, Allison

Serial No.: 10/735,061

Group: Art Unit 2824

Filed: December 11, 2003

Docket: YOR920030136US1 (8728-621)

For: GATED DIODE MEMORY CELLS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO NOTICE OF NON-COMPLIANT AMENDMENT

This is a response to the Notice of Non-Compliant Amendment mailed on 1 August 2007.

In response to said Notice, Applicants submit herewith a revised "Listing of Claims" section (pages 2-6) of the Amendment filed on 24 July 2007, wherein "proper" status identifiers of claims have been provided.

Submitted,

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